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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO		
09/900,224	07/06/2001	Jeffrey D. Carr	45188/FLC/B600	4002		
23363 75	90 09/14/2004		EXAM	EXAMINER		
CHRISTIE, PARKER & HALE, LLP			MOORTHY, ARAVIND K			
PO BOX 7068	SA.: -01-100: 7069		ART UNIT	PAPER NUMBER		
rasadena, (	EA 91109-7068		2131			
			DATE MAILED: 09/14/200	4		
Please find below a	and/or attached an O	ffice communication concernir	ng this application_or_pr	oceeding.		
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	Application No.	Applicant(s)				
•	09/900,224	CARR, JEFFREY D.				
Office Action Summary	Examiner	Art Unit				
	Aravind K Moorthy	2131				
The MAILING DATE of this communication app	ears on the cover sheet with t	he correspondence address				
Period for Reply  A SHORTENED STATUTORY PERIOD FOR REPLY THE MAILING DATE OF THIS COMMUNICATION.  Extensions of time may be available under the provisions of 37 CFR 1.1 after SIX (6) MONTHS from the mailing date of this communication.  If the period for reply specified above is less than thirty (30) days, a reply if NO period for reply is specified above, the maximum statutory period version of the period for reply within the set or extended period for reply will, by statute Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).  Status  1) Responsive to communication(s) filed on 10 S  2a) This action is FINAL.  2b) This action for allowed closed in accordance with the practice under E  Disposition of Claims  4a) Of the above claim(s) is/are pending in the application is/are withdra	Y IS SET TO EXPIRE 3 MON 36(a). In no event, however, may a reply by within the statutory minimum of thirty (30 will apply and will expire SIX (6) MONTHS, cause the application to become ABAND of date of this communication, even if timely eptember 2003.  Exaction is non-final.  Ince except for formal matters.  Ex parte Quayle, 1935 C.D. 11	TH(S) FROM_  De timely filed  It days will be considered timely, from the mailing date of this communication.  ONED (35 U.S.C. § 133).  If filed, may reduce any				
4a)_Of the above claim(s)is/are withdra 5)	WILLIFORN CONSIDERATION					
6) Claim(s) 1-10 is/are rejected.						
7) Claim(s) is/are objected to.						
8) Claim(s) are subject to restriction and/o	r election requirement.	A. A				
Application Papers	The state of the s	A CONTRACTOR OF THE CONTRACTOR				
9) The specification is objected to by the Examine	<u> </u>					
10) ☐ The drawing(s) filed on <u>06 July 2001</u> is/are: a)		to by the Examiner.				
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).						
Replacement drawing sheet(s) including the correct						
11) ☐ The oath or declaration is objected to by the E	xaminer. Note the attached O	ffice Action or form PTO-152.				
Priority under 35 U.S.C. § 119						
12) Acknowledgment is made of a claim for foreign a) All b) Some * c) None of:  1. Certified copies of the priority document 2. Certified copies of the priority document 3. Copies of the certified copies of the priority application from the International Bureat * See the attached detailed Office action for a list	ts have been received. ts have been received in Appority documents have been re ou (PCT Rule 17.2(a)).	lication No ceived in this National Stage				
Attachment(s)						
1) Notice of References Cited (PTO-892)		mary (PTO-413)				
Notice of Draftsperson's Patent Drawing Review (PTO-948)     Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08 Paper No(s)/Mail Date		Mail Date : remail Date : remail Patent Application (PTO-152)				

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#### **DETAILED ACTION**

- 1. Claims 1-10 are pending in the application.
- 2. Claims 1-10 have been rejected.

#### Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A-person-shall-be-entitled-to-a-patent-unless-

(e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371(c) of this title before the invention thereof by the applicant for patent.

The changes made to 35 U.S.C. 102(e) by the American Inventors Protection Act of 1999 (AIPA) and the Intellectual Property and High Technology Technical Amendments Act of 2002 do not apply when the reference is a U.S. patent resulting directly or indirectly from an international application filed before November 29, 2000. Therefore, the prior art date of the reference is determined under 35 U.S.C. 102(e) prior to the amendment by the AIPA (pre-AIPA 35 U.S.C. 102(e)).

3. Claims 1-10 are rejected under 35 U.S.C. 102(e) as being anticipated by Gupta et al U.S. Patent No. 6,378,072 B1.

As to claim 1, Gupta et al discloses a method for concealing parameter transferred between a first and second device, characterized by:

generating by the first device a control signal and a parameter signal [column 3, lines 24-49];

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transmitting by the first device to the second device the control signal and the parameter signal [column 3, lines 24-49];

receiving by the second device from the first device the control signal and the parameter-signal [column-3, lines-24-49]; and

generating by the second device a destination parameter signal using the control signal and the parameter signal [column 3, lines 24-49].

As-to-claim-2, Gupta-et-al-discloses-that-the-method is further-characterized-by-

generating by the first device a first key signal using the control signal [column 5]

lines 21-36]; and

generating by the first device—the parameter signal—by transforming a source parameter signal using the first key signal [column 6, lines 39-56].

As to claim 3, Gupta et al discloses that the method is further characterized by:

generating by the second device a second key signal using the control signal [column 6, lines 39-56]; and

generating by the second device the destination parameter signal by inversely transforming the parameter signal using the second key signal [column 6, lines 39-56].

As to claim 4, Gupta et al discloses that the method is further characterized by:

generating by the first device a key index signal [column 5, lines 37-54];

generating by the first device a key variable signal [column 5, lines 37-54];

transmitting by the first device to the second the key index signal and the key variable signal [column 5, lines 37-54];

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receiving by the second device from the first device the key index signal and the key variable signal [column 6, lines 8-38];

generating by the second device an intermediate key signal using the key index signal and a key table [column 6, lines 8-38], and

generating by the second device the second key signal using the intermediate signal and the variable signal [column 6, lines 8-38].

As to claim 5, Gupta et al discloses generating by the second device the second key signal from the intermediate key signal and the key variable signal using a hash function [column 6, lines 8-38].

As to claim 6, Gupta et al discloses that the method is further characterized by:

parameter signal to generate the parameter signal [column 6, lines 39-56], and

signal an inversely transformed control signal portion [column 6, lines 39-56]; and

comparing by the second device the inversely transformed control signal portion to a portion of the received control signal [column 7, lines 1-27].

As to claim 7, Gupta et al discloses an apparatus for processing a concealed parameter received by a device, characterized by:

a control logic block to receive a control signal and a parameter signal [column 3 line 56 to column 4 line 13];

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an interface operation logic block operably coupled the control signal block generate a destination parameter signal using the control signal and the parameter signal [column 3 line 56 to column 4 line 13].

As to claims 8 and 9, Gupta et al discloses that the apparatus is further characterized by:

a key table module including indexed transformation keys, the key table module operably coupled to the control logic block, the key table module to generate an intermediate key signal using a key index signal received from the control logic block [column 3 line 56 to column 4 line 13];

a key interface stage operably coupled to the key table module and the control logic block for generating a key signal using the intermediate key signal received from the key table module and key variable signal received from the control logic block [column 5, lines 31-54]; and

an inverse transformation module operably coupled to the key interface stage and the control logic block, the inverse transformation module to generate the destination parameter signal by inversely transforming the parameter signal using the key signal received from the key interface stage [column 6, lines 25-56].

As to claim 10, Gupta et al discloses that the apparatus is further characterized by a hash function stage operably coupled to the key interface stage [column 6, lines 25-56]. Gupta et al discloses that the hash function stage is to generate the key signal from the intermediate key signal and the key variable signal [column 6, lines 25-56].

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#### Conclusion

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Ayaz R Sheikh can be reached on 703-305-9648. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Aravind K Moorthy September 8, 2004

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SUPERVISORY PATENT EXAMINER
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